# AN7261FBQ

### FM multiplex reception IC

#### Overview

The AN7261FBQ is an FM-multiplex reception IC including a front-end and an IF block for FM tuner. Since the receiving circuit of FM multiplex signal is integrated on a single chip, it is most suitable for saving the space of sets.

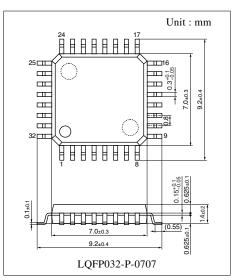
#### Features

- A single chip IC for an FM-multiplex tuner block, which was conventionally composed of two ICs.
- Fewer external components
- Can be used for 15 MHz IF

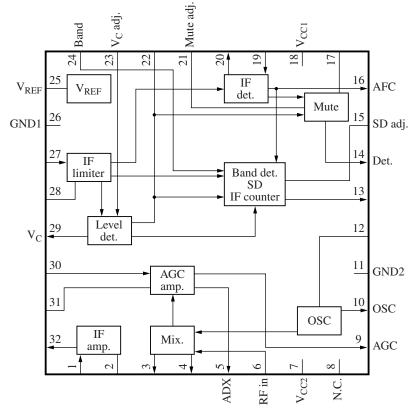
#### Applications

Block Diagram

• VICS, RDS



Note) The package of this product will be changed to lead-free type (LQFP032-P-0707B). See the new package dimensions section later of this datasheet.



#### Pin Descriptions

Pin No.	Description	Pin No.	Description
1	1st IF input	17	Higher freguency correction adjustment
2	GND for 1st IF input	18	V <sub>CC1</sub>
3	Mixer output 1	19	FM detection
4	Mixer output 2	20	2nd IF output
5	PIN diode driver	21	Mute adjustment
6	Mix. Input	22	Mute voltage output
7	V <sub>CC2</sub>	23	Control voltage adjustment
8	N.C.	24	Band mute SW/band signal output
9	AGC output	25	V <sub>REF</sub>
10	OSC buffer output	26	GND1
11	GND2	27	2nd IF input
12	OSC	28	GND for 2nd IF input
13	SD/IF counter output	29	Control voltage output
14	Detection output	30	Keyed-AGC input
15	SD sensitivity adjustment	31	AGC sensitivity adjustment
16	AFC voltage output	32	1st IF output

#### Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	9.1	V
Supply current	I <sub>CC</sub>	66	mA
Power dissipation *2	P <sub>D</sub>	343.6	mW
Operating ambient temperature *1	T <sub>opr</sub>	-30 to +80	°C
Storage temperature <sup>*1</sup>	T <sub>stg</sub>	-55 to +150	°C

Note) \*1:  $T_a = 25^{\circ}C$  except power dissipation, operating ambient temperature and storage temperature.

\*2:  $T_a = 80^{\circ}C$ 

#### Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V <sub>CC</sub>	7.2 to 9.0	V

Parameter	Symbol Conditions		Min	Тур	Max	Unit
Control voltage 1	V <sub>C1</sub>	No signal input, DC voltage of pin 29	0.0	0.05	0.8	V
Control voltage 2	V <sub>C2</sub>	$V_{IN2} = 60 \text{ dB}\mu$ , DC voltage of pin 29	0.7	1.25	1.8	V
Control voltage 3	V <sub>C3</sub>	$V_{IN2} = 80 \text{ dB}\mu$ , DC voltage of pin 29	1.8	2.4	3.0	V
Control voltage 4	V <sub>C4</sub>	$V_{IN2} = 100 \text{ dB}\mu$ , DC voltage of pin 29	3.0	3.7	4.4	V
Control voltage 5	V <sub>C5</sub>	$V_{C5} = V_{C3} - V_{C2}$	1.0	1.2	1.4	V
Control voltage 6	V <sub>C6</sub>	$V_{C6} = V_{C4} - V_{C3}$	1.05	1.25	1.45	V
Detection output level 1	V <sub>01</sub>	$V_{IN2} = 80 \text{ dB}\mu/1 \text{ kHz}, 30\%\text{FM},$ AC voltage of pin 14	70	90	110	mV[rms]
Detection output level 2	V <sub>O2</sub>	$V_{IN2} = 80 \text{ dB}\mu/76 \text{ kHz}, 10\%\text{FM},$ AC voltage of pin 14	37	47	57	mV[rms]
Counter output level	V <sub>IFC</sub>	$V_{IN2} = 100 \text{ dB}\mu$ , $V_{15} = 2 \text{ V}$ , IFC output level			210	mV[rms]
SD sensitivity	SDS	$V_{IN2}$ when $V_{15} = 2 V$ , SD > 4 V	61	71	81	dBµ
SD bandwidth	SD <sub>W</sub>	Bandwidth when SD > 4 V, SD $V_{IN2} = 100 \text{ dB}\mu$	90	115	140	kHz
Limiting sensitivity	V <sub>LIM</sub>	V <sub>IN2</sub> level, 30 kHz LPF on, and 1 kHz 30% FM when pin 14 AC voltage drops by 3 dB	40	48	54	dBμ
Local oscillation buffer output	Vos	f <sub>OSC</sub> = 113 MHz, without input	110	160	210	mV[rms]
IF output level	V <sub>IF</sub>	$V_{IN1} = 80 \text{ dB}\mu$ , IF out level	60	110	170	mV[rms]
AGC sensitivity	S <sub>AGC</sub>	$V_{IN1}$ level when $V_9 = 3 V$	68	72	76	dBµ
High-level AGC voltage	V <sub>AH</sub>	$V_{\rm IN1} = 66 \text{ dB}\mu, V_9$	5.9	6.3	6.7	V
Low-level AGC voltage	V <sub>AL</sub>	$V_{\rm IN1} = 78 \text{ dB}\mu, V_9$		0.05	0.5	V
Supply current	I <sub>TOT</sub>	Without input	41	52	65	mA

#### ■ Electrical Characteristics at V<sub>CC</sub> = 8 V, f<sub>IN1</sub> = 98 MHz, f<sub>IN2</sub> = 15 MHz, T<sub>a</sub> = 25°C

Note) FM tuning condition: Adjust the V<sub>T</sub> of application voltage so that OSC buffer output frequency becomes 113 MHz±10 kHz.

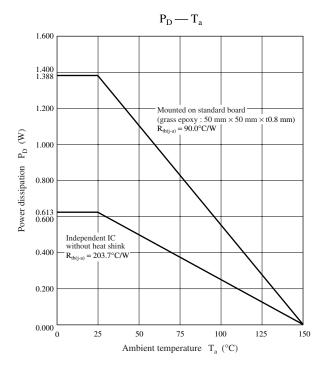
#### Usage Notes

1. Use less than 10 k $\Omega$  of external resistor of pin 31

2. Use this IC not to exceed allowable power dissipation value refering to the "Technical Information".

#### Technical Information

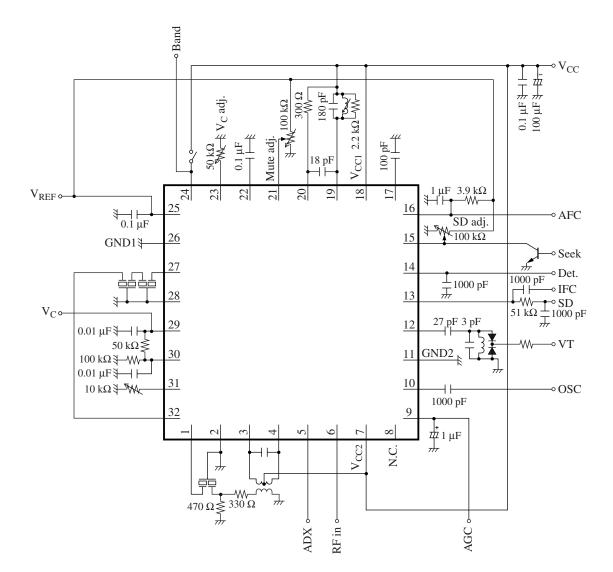
1.  $P_D - T_a$  curves of LQFP032-P-0707



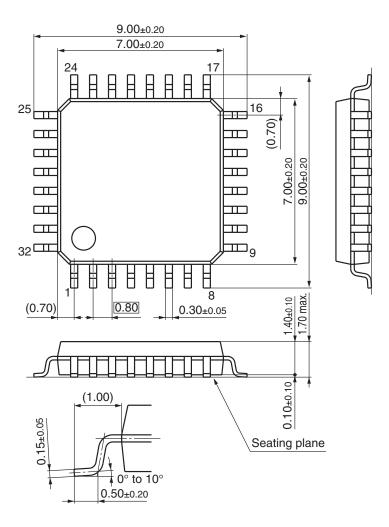
#### 2. Specification for special parts

Part	Maker	Maker part number	Connection	Specification				
Fait			Connection	Pin No.	Turns	LC value	Q at no load	
IF coil	Sumida	4162-T006	$3_{1}$ (4)	4-6	10T	12 pF±10%	50 or more	
				1-2	11T		(15 MHz)	
				2-3	11T			
Detection	Matsushita	SQF5EB033A	(3 4)	1-3	11T	0.59 µH	41±20%	
coil	electronics					±5.0%	(15 MHz)	
	parts							

#### ■ Application Circuit Example



- New Package Dimensions (Unit: mm)
- LQFP032-P-0707B (Lead-free package)



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